PATENT COOPERATION TREATY

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REC'D 1 3 APR 2006

INTERNATIONAL PRELIMINARY REPORT ON PATE TABILITY (Chapter II of the Patent Cooperation Treaty)

PCT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference	FOR FURTHER ACTION	See Form PCT/IPEA/416				
117538 USN						
International application No.	International filing date (day/mo	onth/year) Priority date (day/month/year)				
PCT/SE2003/001917	10-12-2003					
International Patent Classification (IPC) o	r national classification and IPC					
See Supplemental Box						
Applicant	we would a make the Table	\ a+ a]				
Telefonaktiebolaget L	M Ericsson (publ)	, et al				
This report is the international pre Authority under Article 35 and tr	eliminary examination report, esta ansmitted to the applicant accord	ablished by this International Preliminary Examining ling to Article 36.				
2. This REPORT consists of a total						
This report is also accompanied b						
	t and to the International Bureau,					
and/or sheets	sheets of the description, claims and/or drawings which have been amended and are the basis of this report and/or sheets containing rectifications authorized by this Authority (see Rule 70.16 and Section 607 of the					
Administrati	ve Instructions). supersede earlier sheets, but whi	ich this Authority considers contain an amendment that goes				
beyond the d	isclosure in the international app	lication as filed, as indicated in item 4 of Box No. I and the				
Supplementa	l Box.					
b. (sent to the Internati	<i>onal Bureau only)</i> a total of (indi	icate type and number of electronic carrier(s))				
	, containing a se	equence listing and/or tables related thereto, in electronic				
form only, as indicat Administrative Instr		ating to Sequence Listing (see Section 802 of the				
4. This report contains indications r Box No. I Basis of	relating to the following items: of the report					
	_					
Box No. II Priorit		1. 16 language stan of indicated applicability				
		ard to novelty, inventive step and industrial applicability				
	of unity of invention					
Box No. V Reason applic	Box No. V Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement					
1	••					
Box No. VII Certai	n defects in the international appl	lication				
Box No. VIII Certain observations on the international application						
Date of submission of the demand		e of completion of this report				
02-05-2005		-03-2006				
Name and mailing address of the IPEA/SE		horized officer				
Patent- och registreringsverket						
BOX 5055 S-102 42 STOCKHOLM		na Flodman/MP				
Facsimile No. +46 8 667 72 88		ephone No. +46 8 782 25 00				

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.

PCT/SE2003/001917

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In case the space in any of the preceding boxes is not sufficient.

Continuation of: Cover sheet

International patent classification (IPC)

H03B 5/20 (2006.01)

Form PCT/IPEA/409 (Supplemental Box) (April 2005)

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.

PCT/SE2003/001917

Box 1	No. I	Basis of the report				
1.	With r	egard to the language, this report is based on:				
	\boxtimes	the international application in the language in which it was filed				
		a translation of the international application into which is the language of a translation furnished for the purposes of:				
		international search (Rules 12.3(a) and 23.1(b))				
		publication of the international application (Rule 12.4(a))	-0.0			
		international preliminary examination (Rules 55.2(a) and/or 55.3(a))				
2.	furnis	regard to the elements of the international application, this report is based on the to the receiving Office in response to an invitation under Article 14 are referred to the not annexed to this report):	(replacement sheets which have been d to in this report as "originally filed"			
		the international application as originally filed/furnished				
	\boxtimes	the description:				
		pages 1-12	as originally med/turnished			
		pages				
		pages* received by this Authority on				
	\boxtimes	the claims:	as originally filed/furnished			
		pagesas amended (togethe	er with any statement) under Article 19			
		pages				
		pages I Z				
	\square	the drawings:				
		pages 1-8	as originally filed/furnished			
		pages* received by this Authority on				
		a sequence listing and/or any related table(s) see Supplemental Box Relating to	Sequence Listing.			
3.		The amendments have resulted in the cancellation of:				
		the description, pages	•			
		the claims, Nos.				
		the drawings, sheets/figs				
l		the sequence listing (specify):				
		any table(s) related to the sequence listing (specify):				
4.		This report has been established as if (some of) the amendments annexed to the made, since they have been considered to go beyond the disclosure as filed, as if 70.2(c)).	nis report and listed below had not been indicated in the Supplemental Box (Rule			
		the description, pages				
		the claims, Nos.				
		the drawings, sheets/figs				
		the sequence listing (specify):				
		any table(s) related to the sequence listing (specify):				
*	If ite	nn 4 applies, some or all of those sheets may be marked "superseded."				

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.

PCT/SE2003/001917

Box No. V		Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement					
1.	Statement						
Novelty (N)		Claims	1-11	YES	S		

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Claims _______NO

2. Citations and explanations (Rule 70.7)

Documents cited in the International Search Report:

D1: US 4325032 A

D2: US 20020008551 A1

D3: US 6396358 B1 D4: US 6323910 B1

The cited documents represent the general state of the art. The invention defined in claims 1-11 is not disclosed by any of these documents.

The cited prior art does not give any indication that would lead a person skilled in the art to the claimed oscillator circuit with tuneable delay means. Therefore, the claimed invention is not obvious to a person skilled in the art.

Accordingly, the invention defined in claims 1-11 is novel and

Accordingly, the invention defined in claims 1-11 is novel and is considered to involve an inventive step. The invention is industrially applicable.

CLAIMS

1. An oscillator circuit (100, 200, 300, 400, 500), comprising an oscillating element (110, 210, 310, 410, 510) and output means (115, 215, 315, 415) for outputting an oscillation frequency from said oscillating circuit, the circuit further comprising a signal delay means (120, 220, 320, 420, 520, 525) which is arranged in series with the oscillating element and feeds the output signal back to the oscillating element, the delay means (120, 220, 320, 420, 520, 525) being tuneable with respect to the delay it provides, characterized in that the signal delay means (420) is a tuneable delay line comprising a control input, to which is connected the output signal from an amplitude detector (440), with the amplitude detector sampling the signal at a chosen point in the circuit (400) and controlling the delay means so that a maximum output signal is obtained from the oscillator circuit.

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- 2. The oscillator circuit (100, 200, 400) of claim 1, in which the oscillating element is an amplifier (110, 210, 410).
- 3. The oscillator circuit (100, 200, 400) of claim 2, further comprising a filter (130, 230, 430).
 - 4. The oscillator circuit (100, 200) of claim 3, in which the delay means (120, 220) is a Delay Locked Loop.
- 5. The oscillator circuit (100) of claim 4, in which the filter (130) is a pass-band filter connected in series with the delay element (120), so as to filter the signal before it is fed back to the oscillating element (110), but after it has passed through the delay element.
- 30 6. The oscillator circuit (200) of claim 4, in which the filter (230) is a stopband filter connected between the output means (215) and ground, so as to enable only the desired frequency to pass.

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- 7. The oscillator circuit (400) of claim 4, in which the filter (430) is a stopband filter connected between the output means (215) and ground, so as to enable only the desired frequency to pass.
- 5 8. The oscillator circuit (300, 500) of claim 1, in which the oscillating element is a voltage controlled oscillator (310, 510, 515).
 - 9. The oscillator circuit (300) of claim 1 or 8, in which the delay means (320) is a Delay Locked Loop.

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- 10. The oscillator circuit (500) of claim 1 or 8, in which the delay means (520, 525) is a tuneable delay line.
- 11. The oscillator circuit of claim 8 or 10, comprising at least a first and a second VCO (510, 515) and first and second tuneable delay lines (520,525), in which the VCO:s (510, 515), are connected in series via two branches, an "upper" and a "lower" branch, the output of the "lower" branch being used as input to the "upper" branch, and the output of the "upper" branch being used as input to the "lower" branch, with the delay means being connected one between each of the VCO:s, in series with them, so that the upper branch output of the first and second VCO are separated by the same phase distance as their respective lower branch outputs, suitably ninety degrees.